Attribute-Based Signatures for Circuit from Bilinear Map

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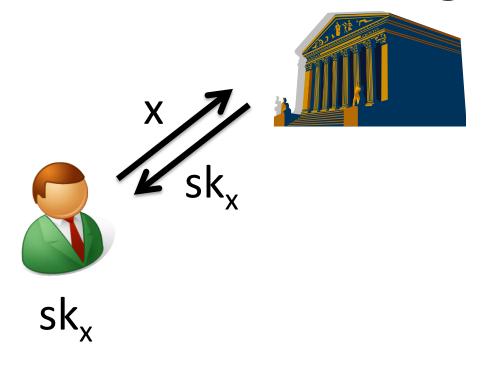
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Our Contribution

- Propose attribute-based signature scheme for arbitrary circuits
 - Secure under SXDH assumption in <u>bilinear groups</u>
 - Fairly practical
 - No a priori bound on size and depth of circuits

The first scheme that simultaneously achieves <u>simplicity of assumption</u>, <u>efficiency</u>, and <u>expressiveness of predicates!</u>

Attribute-based Signatures

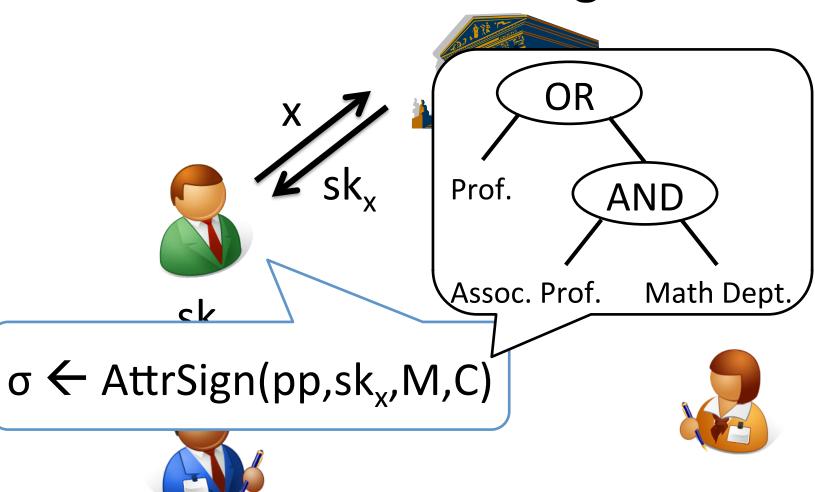


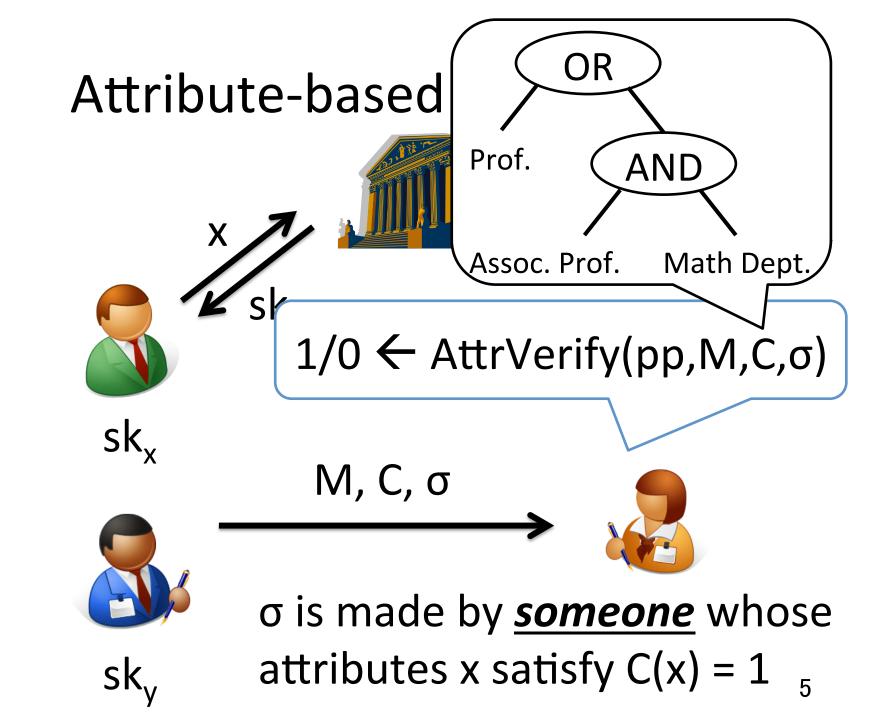






Attribute-based Signatures

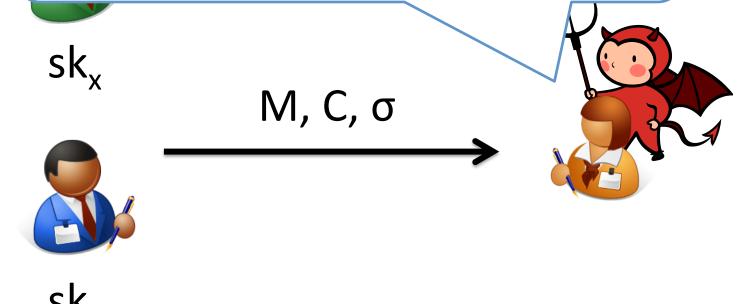




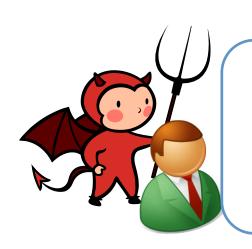
Anonymity



Cannot tell who made σ among signers who satisfy C(x) = 1



Unforgeability



Cannot make valid σ if C(x) = 0

 sk_x



Μ, C, σ



sk

Previous Work

[MPR11] The notion, schemes monotone span programs (bilinear groups)

[OT11] non-monotone span programs (bilinear groups)

[BF14] (1) AND/OR of pairing product equation(2) Arbitrary circuit via Karp reduction (implicit)(generic construction from policy-based signature)

[TLL14] bounded-depth circuits (multilinear maps)

Previous Work

[MPR11] The notion, schemes <u>monotone span programs</u> (bilinear groups)

[OT11] <u>non-monotone span programs</u> (bilinear groups)

[BF14] (1) <u>AND/OR of pairing product equation</u>
 (2) Arbitrary circuit via <u>Karp reduction</u> (implicit)
 (generic construction from policy-based signature)

[TLL14] bounded-depth circuits (multilinear maps)

Previous Work

[MPR11] The notion, schemes <u>monotone span programs</u> (bilinear groups)

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No known scheme simultaneously achieves

simplicity of assumption, efficiency, and

expressiveness of predicates!

(2) Arbitrary circuit via narp reduction (implicit)

(generic construction from policy-based signature)
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[TLL14] bounded-depth circuits (*multilinear maps*)

Our Contribution

- Propose attribute-based signature scheme for arbitrary circuits
 - Secure under SXDH assumption in bilinear groups
 - Fairly practical
 - No a priori bound on size and depth of circuits
- Use NIZK and signature as building blocks
 - Make a <u>"fusion"</u> of Groth-Sahai proofs and Groth-Ostrovsky-Sahai proofs

Groth-Sahai (GS) Proofs

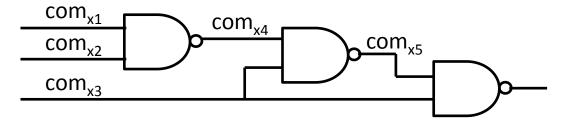
 Non-interactive proofs suitable for proving algebraic equation

$$\prod_{i=1}^{n} e(\mathcal{A}_i, \mathcal{Y}_i) \prod_{j=1}^{m} e(\mathcal{X}_j, \mathcal{B}_j) \prod_{i=1}^{n} \prod_{j=1}^{m} e(\mathcal{X}_i, \mathcal{Y}_j)^{\gamma_{i,j}} = T$$

- Proof consists of two phases:
 - To commit to the witness group elements
 - To prove the elements committed to satisfies the equation to be proven

Groth-Ostrovsky-Sahai (GOS) Proofs

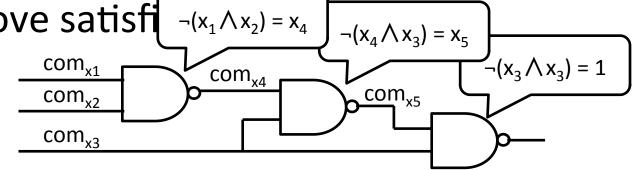
 Non-interactive proofs system which can prove satisfiability of circuits



- Proof consists of two phases:
 - To commit to each assignments to wires
 - To prove the assignments committed to follows input/output relation of each gate

Groth-Ostrovsky-Sahai (GOS) Proofs

• Non-interactive proofs system which can prove satisfi $\neg(x_1 \land x_2) = x_4$ $\neg(x_4 \land x_2) = x_5$



- Proof consists of two phases:
 - To commit to each assignments to wires
 - To prove the assignments committed to follows input/output relation of each gate

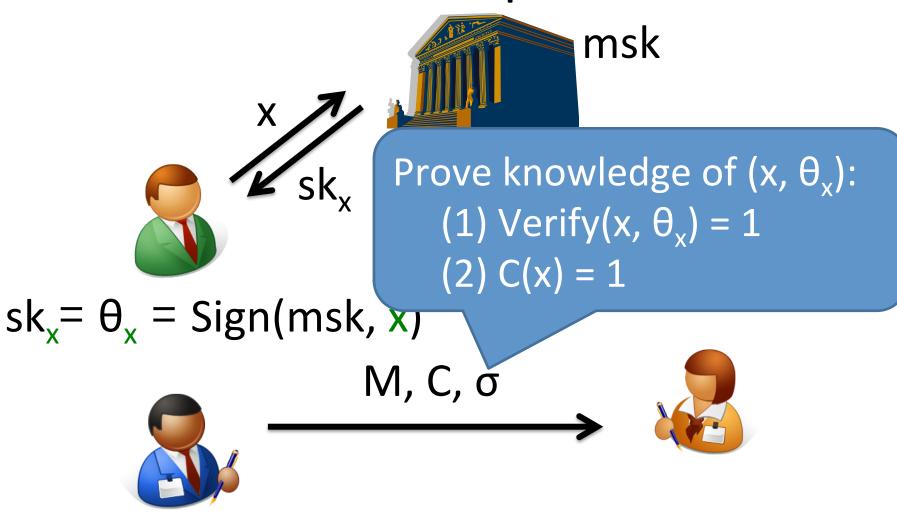


$$sk_x = \theta_x = Sign(msk, x)$$





$$sk_y = \theta_y = Sign(msk, y)$$



$$sk_v = \theta_v = Sign(msk, y)$$

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Prove knowledge of (x, \theta_x):

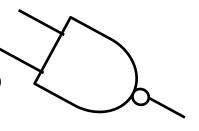
(1) Verify(x, \theta_x) = 1

(2) C(x) = 1
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- GS proofs are suitable for (1), while
 GOS proofs are suitable for (2)
- If we have a "fusion" of GS proofs and GOS proofs...?



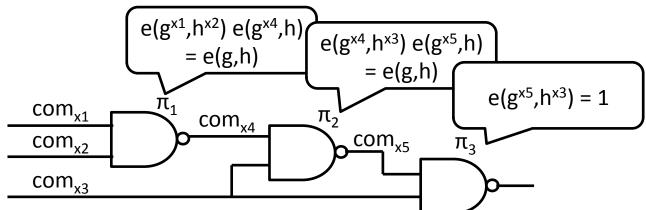
"Fusion" of GS and GOS

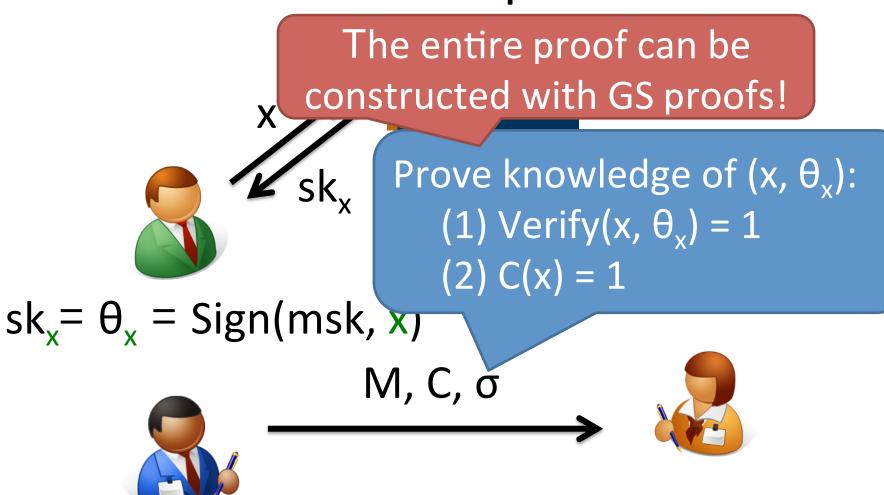


- Both follow the <u>"commit-and-prove"</u> structure
- Translate $\frac{(x \wedge y) = z''}{y}$ into algebraic equation
- $\neg(x \land y) = z$

$$\Leftrightarrow 1 - xy = z$$

 \Leftrightarrow $e(g, h) e(g^x, h^y)^{-1} = e(g^z, h)$

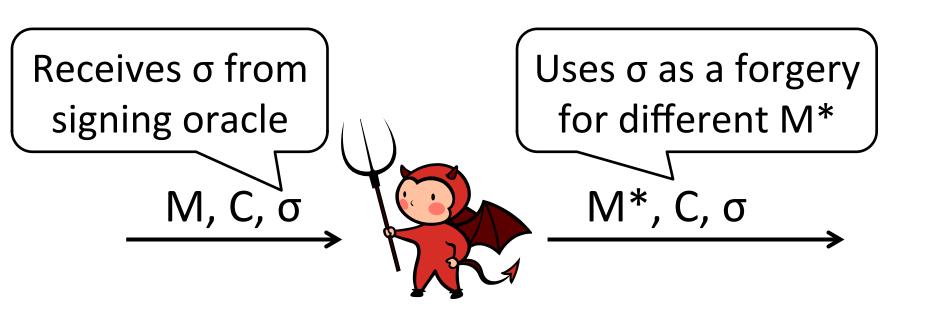




$$sk_v = \theta_v = Sign(msk, y)$$

But...

- It doesn't provide <u>CMA security</u>
- σ is not bound to M



Dummy Attribute [MPR11]

Prove knowledge of (x, θ_x) : θ_x is

(a) Signature on $\frac{OR}{x \text{ s.t. } C(x) = 1}$

(b) Signature on dummy t defined by M

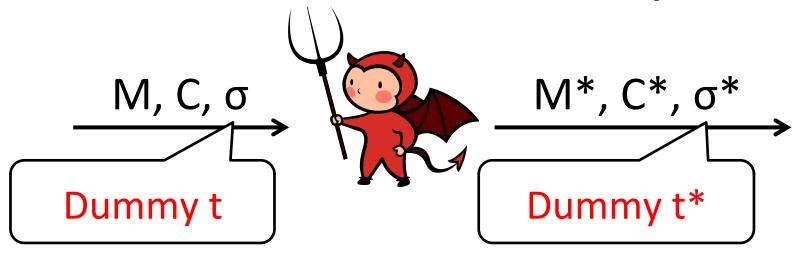
<u>Point</u>

Use different t for different M

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[MPR11] Hemanta K. Maji, Manoj Prabhakaran, Mike Rosulek: Attribute-Based Signatures. CT-RSA 2011: 376-392

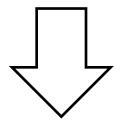
Intuition for Security



- If adversary sees (M, C, σ) and outputs (M*, C*, σ*), the reduction
 - uses <u>signature on t</u> for simulating σ ,
 - extracts **signature on t*** from σ^*
 - Reduction works successfully because t ≠ t*

Main Theorem

<u>Theorem</u> Non-interactive proof system is witness-indistinguishable and extractable, signature scheme is unforgeable, the proposed scheme is anonymous and unforgeable



Instantiate this with GS proofs in SXDH setting and Kiltz-Pan-Wee structure-preserving signature

Theorem If SXDH assumption holds, the proposed scheme satisfies anonymity and unforgeability



Performance

	Signature size [Group Elements]	Assumption	Predicate
[MPR11] (1)	36s+2t+24ks	q-SDH, SXDH	Monotone Span Program
[MPR11] (2)	28s+2t+12k+8	SXDH	Monotone Span Program
[MPR11] (3)	s+t+2	Generic Group	Monotone Span Program
[OT11]	9s+11	DLIN	Non-Monotone Span Program
Ours	12ℓ+20N+26	SXDH	Non-monotone Circuit

k: Security parameter

sxt: Size of span program

ℓ: Input size of circuit

N: Number of gates in circuit

Almost same performance as previous schemes while more expressive!!!



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